

09/12/03

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Transmitted herewith for filing is the patent
application for:

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EXPRESS MAIL NO: EV322694925USDATE OF DEPOSIT: September 12, 2003NAME: Justin GoreeSIGNATURE: Justin Goree

Inventors: **Rajesh KAUSHIK**
Rajesh NARWAL

For: **A CMOS BUFFER WITH REDUCED GROUND BOUNCE****Enclosed are:**

- ☒ Patent Application: 17 pages, 17 claims.
7 sheets of drawings.
☒ The suggested drawing figure to be published is FIG. 6.
☒ Citation Under 37 CFR 1.97 and PTO-1449.
☒ Assignee info:
Name: **STMicroelectronics Ltd.**
Address: **Plot No. 2 & 3, Sector 16A, Institutional Area,
Noida-201 3001, Uttar Pradesh, India**
Country of Incorporation: **India**
☒ Applicant claims priority benefit to the following U.S. application(s):
Application No.: **10/206,135**
Filing Date: **JULY 26, 2002**
☒ Applicant claims priority benefit to the following foreign application(s):
Country: **India**
Application No.: **804/Del/2001**
Filing Date: **July 27, 2001**

The Declaration and Filing Fee are NOT ENCLOSED.

- ☒ Name, Address and Citizenship of Inventor(s) is as follows:

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Rajesh NARWAL

- ☒ **PLEASE ADDRESS ALL CORRESPONDENCE TO ATTORNEY OF RECORD:**
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- ☒ Associate this file with Customer No. 27975.



27975

PATENT TRADEMARK OFFICE

Date: September 12, 2003

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